



MOD6321-R WirelessHD Receiver Module

Data Sheet

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Glossary

A glossary of terms used in this document.

Acronym	Definition
BB	Baseband
CBUS	Control Bus
DDC	Display Data Channel
ESD	Electrostatic Discharge
FPC	Flexible PCB Cable
FS	Full Speed
HDCP	High-bandwidth Digital Content Protection
HDMI	High-Definition Multimedia Interface
HPD	Hot Plug Detect
HRTR	Defined in the WirelessHD specification. A device that is capable to both transmit and receive the High Rate PHY (HRP).
I ² C	Inter-Integrated Circuit
MHL	Mobile High-Definition Link

1. General Description

The MOD6321-R/MOD6321-4K-R WirelessHD Receiver Module, also known as SK63101, is a WirelessHD module for highly integrated display devices and receiver adapters. The receiver module provides a digital audio video interface compatible with HDMI, which allows reception of high quality video up to 1080p (MOD6321-R) or 4K (MOD6321-4K-R) from any

WirelessHD-compliant transmitter. End consumers can use a system equipped with this module to wirelessly connect their Digital TV to high quality WirelessHD video sources by bringing them within range and powering them on.

The MOD6321-R module fits into a variety of industrial designs. Standalone software features and user interfaces are predefined; therefore, no software customization at the module level is required to achieve a complete WirelessHD receiver system independent from the wired connectivity offered.

The MOD6321-R module is a completely self-contained autonomous WirelessHD subsystem that connects to a system board that provides the wired connectivity to a digital display. The advantage of this design is that the complexity of the wireless system, radio performance, regulatory requirements, and compliance to standards are all eliminated. This system interface carries video, audio, power, and control signals. The module is precertified and is fully tested for fast time-to-market.

1.1. Applications

- Receive adapter
- Digital TV
- Video Projector
- Portable Display
- AV receiver
- Commercial, industrial, and medical displays and monitors

1.2. Features

- WirelessHD V 1.1 compliant device
- 60 GHz interference free link for up to 4 Gbps Video data Rate
- Small form factor module
- Wide support for video resolutions
 - VGA through SXGA+
 - 480i/576i to 1080p/60 Hz (MOD6321-R)
 - 480i/576i to 4K/30 Hz (MOD6321-4K-R)
 - 3D video support 720p/1080p
- Subframe latency video for real time control of interactive content, such as video games
- Support for surround sound audio
- Support for CEC or AVC commands
- HDCP Content protection
- Automated advanced power control, for energy saving operation



2. Module Program Deliverables

- Evaluation hardware is available. Contact Lattice Semiconductor distributors for more information on starter kit availability.
- Receiver module with the Sil6321 HRRX network processor and the Sil6312 HRTR RF transceiver
- Module data sheet
- I/O board reference schematic for connecting to the module
- Mounting bracket and thermal plate reference
- Control/debug interface connector and cable specification
- Turnkey factory test setting and tools (SWAM3)
- FPC schematics and layout

3. Module Features

- Sil6321 HRRX network processor
- Sil6312 HRTR RF transceiver
- Standalone operation
- Automatic WirelessHD network management
- Automatic advanced power management control
- I²C port for advanced features
- Integrated USB debug and in-system firmware update access port with driver and tool suite
- Feature rich optional API for optional comprehensive GUI integration
- Operates at single 3.3 V
- High density single connector for Flexible PCB cable connection, or board to board connection
- Integrated UI port for status LED and button
- Certified for major worldwide regulations



4. Mechanical Definition

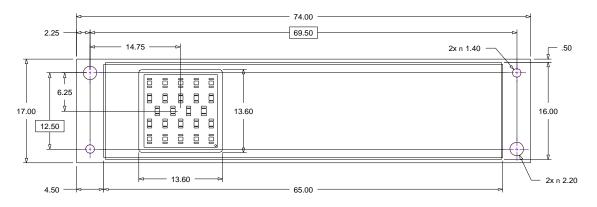
4.1. Module Dimensions

The small footprint of the MOD6321-R receiver module allows for placement in a wide range of industrial designs.

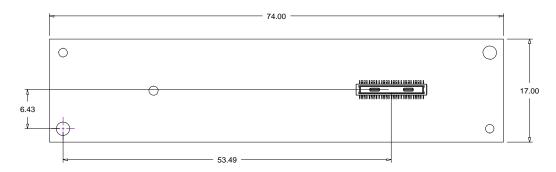
The MOD6321-R receiver module PCB board dimensions are 74 mm x 17 mm x 5.4 mm.

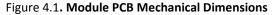
The MOD6321-R receiver module is intended to be mounted to a system chassis with ISO 7045:2011 M2x0.4 pan head screws with type H or type Z cross recess.

The MOD6321-R receiver module comes with mounting features for direct assembly to a predefined metal bracket, providing thermal conductivity, grounding planes, positioning pins, and radio emission protection.









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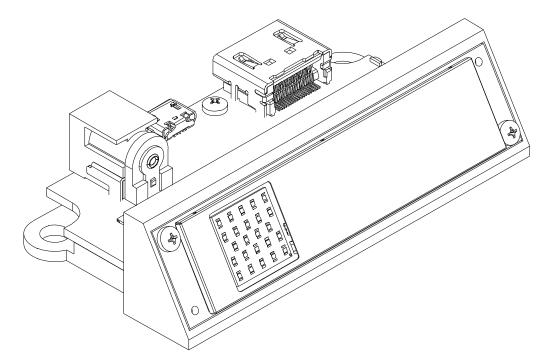


Figure 4.2. Module Installation Example in a Single Port Adapter System

4.2. Module Interface Connector

The module is designed to be connected to the system board through a single board to board connector:

On the module side:

- The connector type: DF40 from Hirose
- Part number: DF40C-50DP-0.4V(51)
- Description: dual row board to board receptacle (plug), 0.4 mm pitch, 50 pins

On the system side the following connector should be used to mate with the module:

On the system side:

- The connector type is: DF40 from Hirose
- Part number: DF40C-50DS-0.4V(51)
- Description: dual row board to board socket, 0.4 mm pitch, 50 pins

4.3. Module/System Cable

One practical way of connecting the module to a system is to use a Flexible PCB Cable (FPC) solution.

This solution has been qualified by SiBEAM with the following considerations:

- Three-layer FPC with signals on the inner copper layer and ground plane on the bottom and top layers.
- Signals are routed with:
 - Four 100Ω differential pairs at 3.6 mil trace/6 mil space
 - One 90 Ω differential pair at 4 mil trace/4 mil space
 - Single-ended traces at 4 mil trace/8 mil space
- Cable dimension:
 - Width: W = 15 mm
 - Length: L < 100 mm
 - Thickness: Z < 0.4 mm

This particular implementation has been tested with cable length up to 100 mm and gives successful HDMI eye pattern test up to 1080p60 Hz when used with SiBEAM I/O reference system board. It also provides sufficient radiation control to satisfy most regulation requirements.



Three-layer Nominal Stacks Up

Table 4.1. FPC Cable Three-layer Nominal Stack-up

Circuit Layer	Thickness	Unit	Material	FPC Construction
-	13	μm	Polyamide	Top Coverlay
-	25	μm	Adhesive	Top Coverlay
Тор	35	μm	Copper	
_	20	μm	Adhesive	Single-sided FPC
-	50	μm	Polyamide	
_	40	μm	Adhesive	Bonding Sheet
Signal	18	μm	Copper	
-	20	μm	Adhesive	
-	50	μm	Polyamide	Double-sided FPC
-	20	μm	Adhesive	
Bottom	35	μm	Copper	
-	25	μm	Adhesive	Pottom Covorlay
-	13	μm	Polyamide	Bottom Coverlay
Total	364	μm	—	

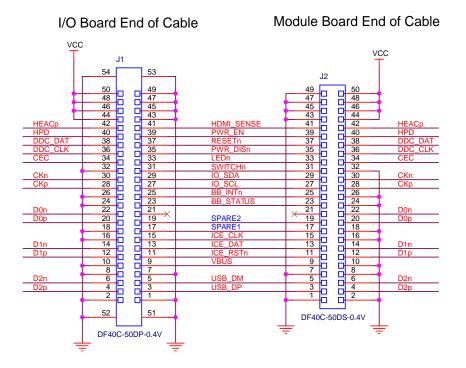
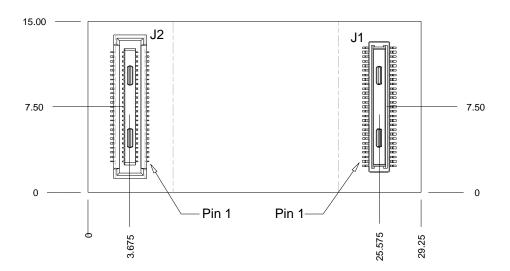
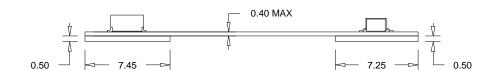


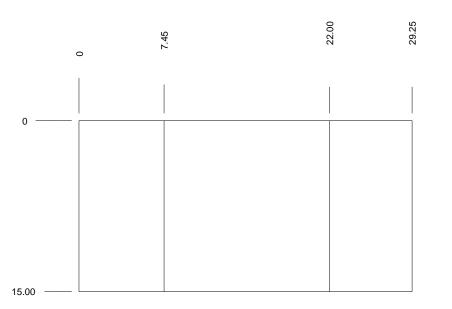
Figure 4.3. System to Module FPC Cable Schematics

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4.4. Module Placement in a System

As in any wireless application, the designer needs to pay special attention when integrating MOD6321-R WirelessHD Receiver Module in a system in order to gain full benefit from the radio performance.

The technology used in this module is a highly advanced antenna phase array with automated beam steering. This implies the Sil6312 RF transceiver chip actually emits a directional radio signal at a wide range of angles from the antenna surface plane.

4.4.1. Placement Advice

- Place the Module inside the system so the SiI6312 HRTR transceiver faces the room in the most natural system placement
- Avoid placing any metallic item in front of Sil6312 HRTR transceiver
- Any polymer based enclosure, like ABS, PE, PA material is recommended as a cover for the Sil6312 HRTR transceiver
- Polymer material enclosure wall thicknesses below 2 mm are recommended as they are nearly transparent to the 60 GHz radio signal
- Consider placing the module tilted up between 30 and 60 degrees to cover cases where the transmitter is placed below the receiver, in a TV shelf for example
- Consider an air gap of 2 mm or more between the Module SiI6312 HRTR transceiver and the covering enclosure wall for three reasons:
 - Skin temperature considerations due to heat dissipation from the RF transceiver, which can reach up to 90 °C max in operation
 - Warping and discoloration of the enclosure due to heat from the Sil6312 HRTR transceiver
 - Allows the link to be maintained even when a blocking object is right in front of the antenna device touching the enclosure

4.4.2. Conditions to Avoid

- Mounting screws in front of the Sil6312 HRTR transceiver
- Metallic paint finishes over the enclosure covering the SiI6312 HRTR transceiver
- Metallic mesh in front of the Sil6312 HRTR transceiver
- Plastic enclosure with complex ribbing in front of the SiI6312 HRTR transceiver
- Glass material in front of the Sil6312 HRTR transceiver
- Narrow opening window in front of the Sil6312 HRTR transceiver

4.4.3. Module-to-System Mechanical Interface

The system is expected to provide a mechanical fixture to addressing the following aspect of the design:

- Mounting brackets to hold the module and the cable in place according to drop test and shake test expectations
- Thermal conductive contact for the module to remove its heat from the inside of the host system
- Grounding requirements for the module to the system ground
- The system mechanical mount is required to provide an EMI shield for the back side of the module. It is therefore necessary that this fixture seals the back of the PCB to avoid spurious emissions

For this to be accomplished, SiBEAM defined the footprint of the mounting bracket which has been tested to realize the conditions above as a reference.



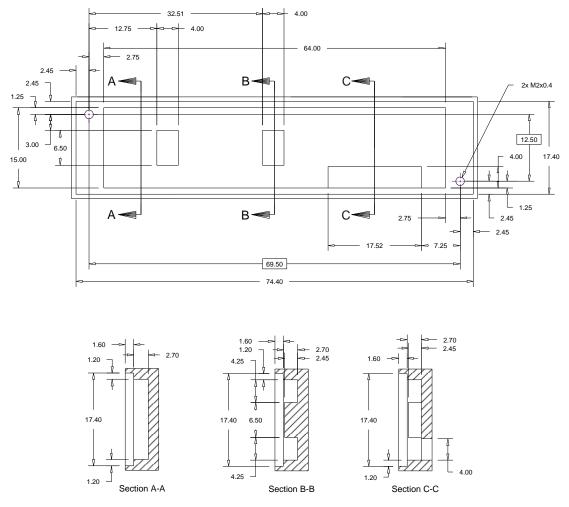


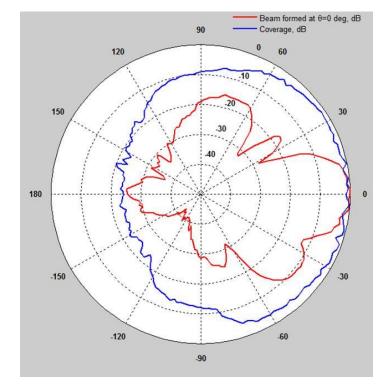
Figure 4.5. Mounting Fixture Recommended for MOD6321-R Module

4.4.4. Module Radiation Pattern

To help the system designer find the best placement inside the enclosure, the following radiation pattern should be considered:

- MOD6321-R Module Radiation Pattern Azimuth (Figure 4.6)
- MOD6321-R Module Radiation Pattern Elevation (Figure 4.7)







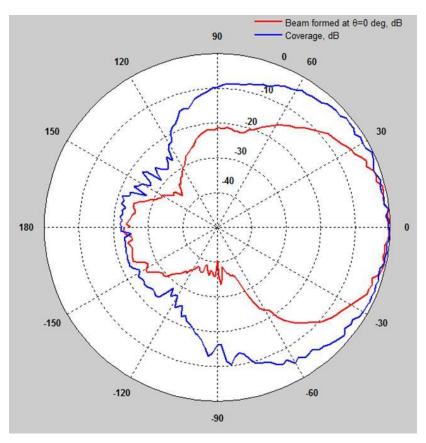


Figure 4.7. MOD6321-R Module Radiation Pattern Elevation (View from Side)



4.4.5. Guard Area Solid Angle

This is the recommended minimum guard area in front of the Sil6312 RF transceiver, to ensure maximum radio performance in the end system.

The guard area is a solid angle where no blocking features should reside.

Blocking features are mechanical items like metallic mounting screws, washers, wire mesh, thick plastic ribs, glass items, metallic paint, metallic stickers and labels, and logos and emblems.

If unsure, consult SiBEAM before committing to the system industrial design tooling.

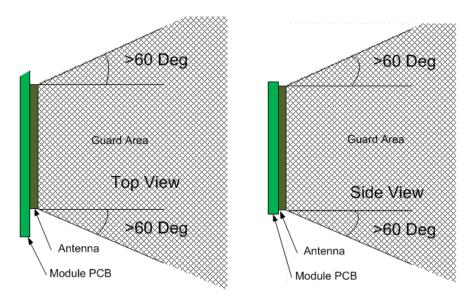


Figure 4.8. Minimum Recommended System Guard Area



4.4.5.1. System Performance Target

The MOD6321-R module is intended to be integrated into the end product, giving the performance described in Figure 4.9.

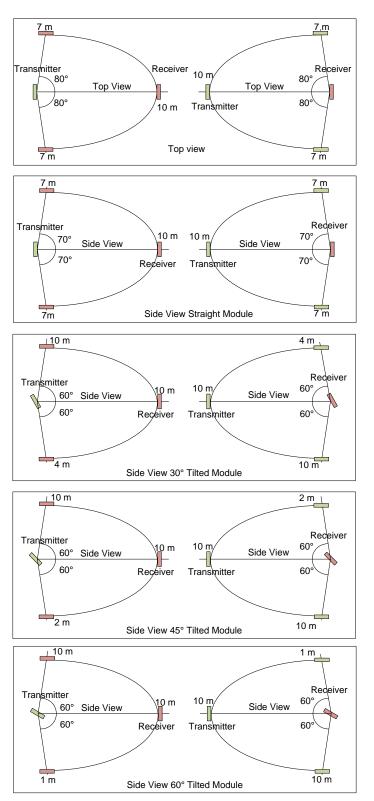


Figure 4.9. Expected System Performance Integrating MOD6321-R Module



5. Thermal Considerations

The MOD6321-R receiver module is designed to be integrated in a complete system capable of outputting digital audio and video.

This module is specified to operate in an ambient temperature from 0 °C to 60 °C. The system design has to provide adequate heat dissipation to keep the ambient temperature inside the system chassis from exceeding 60 °C.

The module design associated with the reference thermal fixture should guarantee that within the 0 °C to 60 °C ambient range. None of the module ICs, especially the baseband and RF chipset, ever exceeds 90 °C case temperature.

This should allow the customers, depending on system design, to specify that the end product integrating the MOD6321-R operates within a 0 °C to 40 °C ambient range.

5.1.1. Thermal Test Conditions

In order to validate the thermal performance of the module design, the following test has been carried out:

Test Setup:

- The module is mounted to the reference aluminum fixture with thermal pads
- Temperature probes are fixed on the BB IC and the RF IC package
- This assembly is set inside a temperature chamber
- A WirelessHD transmission at 1080p 60 Hz is established

Test Case:

- Case temperature on both ICs is monitored
- Audio-video link condition is monitored
- The chamber raises temperatures from 0 °C to 60 °C at 10 °C interval with a soaking time of 20 minutes

Test Conditions:

- 1. The audio-video link is maintained at all time with stable conditions
- 2. The IC case temperature never exceeds 90 °C

Test Results:

- If condition 1 and 2 are satisfied, the test result is: PASS
- If condition 1 or 2 are not met, the test result is: FAIL

Table 5.1 shows the results of this test.

Table 5.1. Thermal Performance

Ambient Temp °C	BB Tc °C	RF Tc °C	Video Link	Test
0	29.8	30.5	Stable	PASS
10	44.9	45.7	Stable	PASS
20	52.5	52.3	Stable	PASS
30	59.1	58.6	Stable	PASS
40	64.1	63.8	Stable	PASS
50	72.8	72.4	Stable	PASS
60	77.5	77.4	Stable	PASS

Soaking time 20 minutes

Note: Module temperatures exceeding 90 °C case temperature should not be destructive for the module. When the WirelessHD chipset operating conditions are exceeded, the video link drops, the module shuts down, and it cools until environmental conditions allow the module to operate normally. This can be taken into account during safety and reliability testing; however, this feature should not be considered as a normal operating condition.

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5.1.2. Thermal Fixture Design

This section describes the Module thermal features to be considered by WirelessHD module integrators. Two copper ground areas on the bottom face of the module are specifically designed to be used for heat dissipation. SiBEAM recommends making good use of those features to help drain the calories accumulated on the module. Each area is specifically designed to remove heat from the SiI6321 and the SiI6312 integrated circuit.

Two methods are viable and can be considered to conduct heat from module to the system thermal fixture:

- Thermal grease
- Thermal pad

The choice between the two and the characteristic of those are highly dependent on the thermal design on the system side.

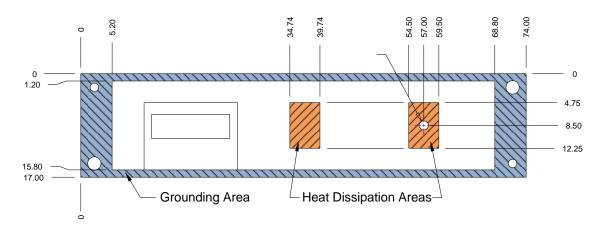


Figure 5.1. Heat Dissipation Areas

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6. Electrical Interface Definition

This section describes the electrical signals and capabilities defined between the module and the host system. This interface provides great versatility for a wide range of end-product applications.

6.1. Single Connector Features

The MOD6321-R receiver module is equipped with a single system interface featuring:

- Power: 3.3 V @ 1 A
- System Control:
 - Status LED signal
 - Source switch signal
 - ON/OFF control signal
 - Sleep mode indication signal
 - ON/OFF indication signal
- Advanced control port
 - I²C interface
- Debug, control, and flash programing
 - USB interface
- Digital video output signal compatible with HDMI signals on the system board

6.2. Application Examples

This interface allows a wide variety of system configuration as illustrated below.

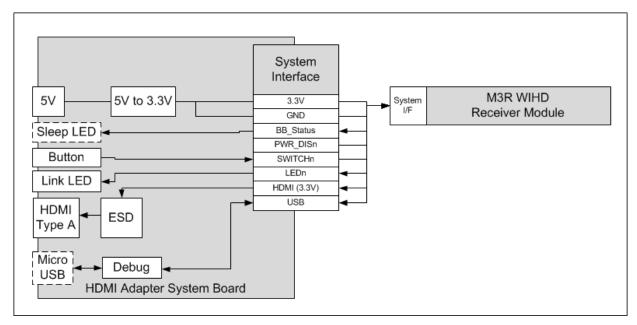


Figure 6.1. MOD6321-R (M3R) Electrical Interface for Single Port Receiver Adapter



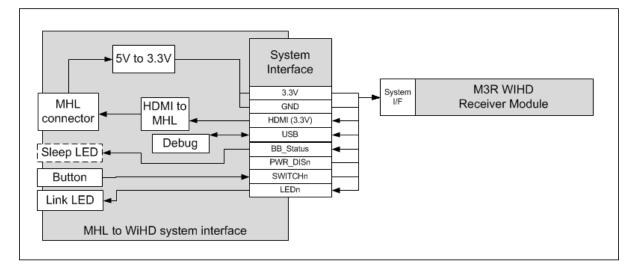


Figure 6.2. MOD6321-R (M3R) Electrical Interface for MHL TV Dongle

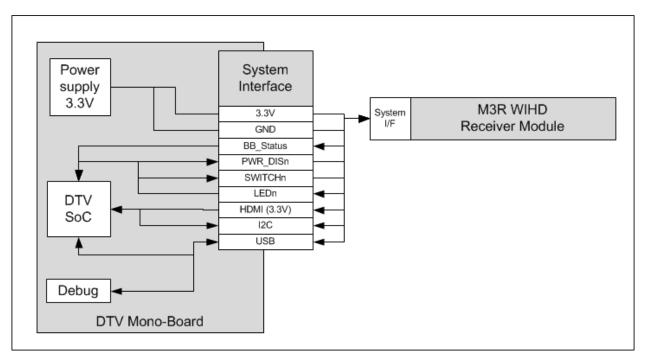


Figure 6.3. MOD6321-R (M3R) Electrical Interface for WirelessHD Integrated DTV

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7. System Interface

This section describes the Electrical Interface between the MOD6321-R receiver module and the host system.

7.1. Connector Pin-out

Table 7.1. System Interface Pin-out

Name	Pin Number	Signal	Туре	Description	Function Group
GND	1, 2, 7, 8, 10, 16, 18, 24, 26, 32, 43, 45, 47, 49.	Electrical Ground	-	Ground.	Module power
VIN	44, 46, 48, 50	3.3 V ±5%, 1 A	_	Single Power Supply Input.	Module power
RSVD/NC	17, 19, 21, 41,	N/C	_	Reserved pin, not connected.	Reserved pins
USB-DP	3	USB	_	USB Data Positive.	On-board debug
USB-DM	5	USB	_	USB Data Negative.	On-board debug
V-BUS	9	USB Power +5 V	Ι	USB Detection.	On-board debug
ICE-RST	11	LVCMOS	-	Microcontroller code update.	Module manufacturing
ICE-DAT	13	LVCMOS	I/O	Microcontroller code update.	Module manufacturing
ICE-CLK	15	LVCMOS	CLK	Microcontroller code update.	Module manufacturing
RESETn	37	LVCMOS	Ι	Baseband Reset, Active LOW.	On-board debug
BB-INTn	25	LVCMOS	0	Baseband Interrupt, Active LOW.	System control option
I ² C-SCL	27	LVCMOS	CLK	Module I ² C Control.	System control option
I ² C-SDA	29	LVCMOS	I/O	Module I ² C Control.	System control option
SWITCHn	31	LVCMOS	I	System control, Active LOW.	WirelessHD control
LEDn	33	LVCMOS	0	Wireless Status, Active LOW.	WirelessHD status
PWR-DISn	35	LVCMOS	I	Wireless Disable, Active LOW	System control option
PWR-EN	39	LVCMOS	0	Power State.	System control option
BB_STATUS	23	LVCMOS	0	Baseband State.	System control option
HDMI-D0p	20	TMDS	0	HDMI Data pair 0 Positive.	Digital video output
HDMI-D0n	22	TMDS	0	HDMI Data pair 0 Negative.	Digital video output
HDMI-D1p	12	TMDS	0	HDMI Data pair 1 Positive.	Digital video output
HDMI-D1n	14	TMDS	0	HDMI Data pair 1 Negative.	Digital video output
HDMI-D2p	4	TMDS	0	HDMI Data pair 2 Positive.	Digital video output
HDMI-D2n	6	TMDS	0	HDMI Data pair 2 Negative.	Digital video output
HDMI-CKp	28	TMDS	0	HDMI Clock Positive.	Digital video output
HDMI-CKn	30	TMDS	0	HDMI Clock Negative.	Digital video output
HDMI-CE	34	LVCMOS	I/O	HDMI CEC.	Digital video output
DDC-CLK	36	Open Drain 5 V tolerant	CLK	HDMI Control Clock, an external pull-up of 2 kΩ to HDMI_5V is required.	Digital video output
DDC-DAT	38	Open Drain 5 V tolerant	I/O	HDMI Control Data, an external pull-up resistor of 2 k Ω to HDMI_5V is required.	Digital video output
HDMI-HPD	40	Open Drain 5 V tolerant	I	HDMI Hot Plug Detect, an external 15 kΩ and a 0.1µF 16V capacitor are required	Digital video output
HDMI-HEAC	42	LVCMOS	Ι	HDMI Audio Return Channel.	Digital video output



7.2. Module Block Diagram

The Module block diagram is described below.

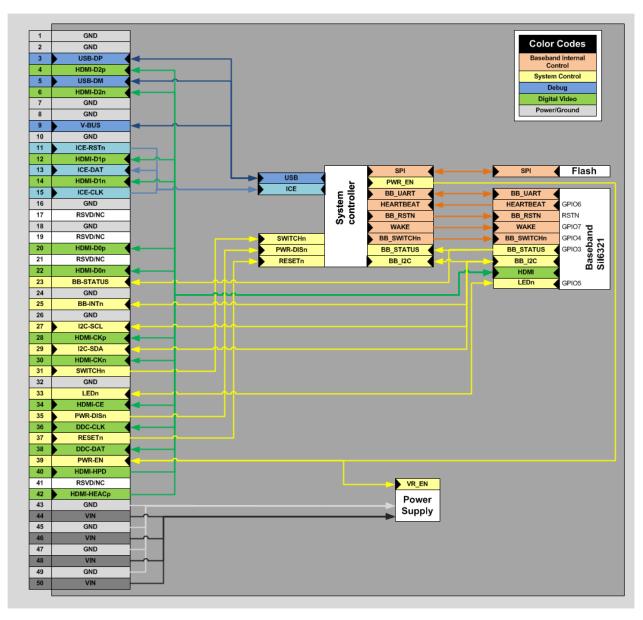


Figure 7.1. MOD6321-R Module Block Diagram

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7.3. Signal Description

7.3.1. Module Power

7.3.1.1. VIN

The 3.3 V power input must be connected to the system power supply and be able to supply 1 A of continuous power with a ripple of less than $\pm 5\%$.

Due to the nature of the wireless power optimization implemented in this chipset, the average power consumed by the module is much lower than the peak power, which is transient. So even though the power supply has to be able to sustain 1 A peak current, the typical current drawn on this power input is below 0.6A.

7.3.1.2. GND

This pin provides the common power and signal ground for the module.

7.3.2. On-board Debug

7.3.2.1. USB

The module is equipped with an on-board microprocessor which provides a direct interface to the system for debugging, testing, and manufacturing.

The three signals USB-DP, USB-DM and V-BUS can be routed to the system board where a USB connector can be mounted. This way, a simple USB cable to a PC can give all debug, test and manufacturing capabilities.

This USB interface is the port to be used for firmware update, at the factory, at the service center, or in the field.

The USB port functions with a proprietary driver and application provided in the software tool suite called SWAM3.

The USB driver is USB2.0 compatible and uses VID = 0x1A4A and PID = 0x6325 as driver signature.

SWAM3 software suite can be provided. Ask your Lattice Semiconductor sales representative.

The functions and use of this tool are documented separately.

7.3.3. Module Manufacturing

7.3.3.1. In-circuit Emulation

Signals USB-DP, USB-DM and V-BUS are used for programming and debugging the microcontroller onboard the module. They are only needed for module manufacturers and developers and are not needed at system level. The module ships preprogrammed and this microcontroller software cannot be updated in the field or by module integrators.

7.3.4. System Control Option

7.3.4.1. I²C, RESETn, and BB-INTn

These optional signals are used for advanced control of the module in feature-rich systems. The module is capable of standalone operation without any system host control, but in some cases the system would want to take control of the module through the I²C interface, for example:

- System host wants to upgrade the baseband firmware
- System host wants to query the radio signal strength
- System host wants to force the module in specific configurations

In the cases above, the system designer would route the signals above to its own system host processor.

Note: BB-INT*n* is the interrupt line from baseband to system host. This interrupt signal is necessary to handle any I²C message transactions.



7.3.5. WirelessHD Control/Status

7.3.5.1. SWITCHn (Active LOW)

This input signal to the module is a mandatory function for the module to operate in the WirelessHD network. It allows the user to connect the transmitter module to any existing network or to switch from one network to another.

This signal can be routed to a physical switch on the system that the user can access to or it can be routed to a host processor to offer control to the user.

In all cases, this function is required for the user to control the module.

This signal has a short press function and a long press function which depends on the module state:

- When the module is ON:
 - Short press (< 10 seconds): NEXT-SOURCE. This function allows users to switch the source of the audio/video to another WirelessHD transmitter in range. This disconnects the current video connection and triggers a connection to the next source available in the current network device list. Each time the signal goes LOW, the audio/video source switches to the next video source, until the MOD6321-R receiver module is reconnected to its original audio/video source. If there is only one WirelessHD audio/video source transmitter in range, the short press will not have any effect because there is no video source to switch to.
 - Long press (> 10 seconds): RESET to FACTORY. This function allows users to reset the MOD6321-R receiver module to its original shipping condition. All run time settings are erased from memory. The main use of this function is to set the current channel setting to factory default, which is LR2 HR2.
- When the module is OFF or in Power Save:
 - Any press: WAKE This function sets the MOD6321-R receiver module to the ON state.

Example of application circuit for SWITCHn signal to a user button on the system board.

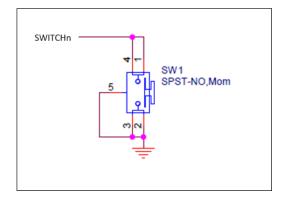


Figure 7.2. SWITCHn Signal Connection Example on System Board

7.3.5.2. LEDn (Active LOW)

This output signal from the module is a mandatory indicator of the state of the module in the context of a WirelessHD network. It indicates if the module is attached to a network, searching for a network, or connected to a receiver.

This signal can be routed to a physical LED on the system that the user can view, or it can be routed to a host processor.

This indication must be available for the user to understand the state of the wireless link.

This pin shows the state of operation of the MOD6321-R receiver module to the user.

The LEDn signal indicates four states:

- **LED is OFF.** The module is not powered, has not yet started, or is in Power Save mode.
- **LED is blinking slowly.** The module has started but has not yet associated with any network.
- **LED is blinking fast.** The module is attached (associated as described in the WirelessHD Specification) to a network but is disconnected (or not yet connected), meaning no audio/video connections are currently processed.
- LED is ON. The module is hosting an ongoing audio/video connection with a station in the network.

Example of application circuit for LEDn signal to a user LED display on system board:

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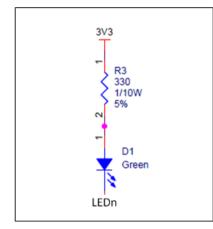


Figure 7.3. LEDn Signal Connection Example on System Board

7.3.5.3. PWR-DISn (Active LOW)

The Power Disable function is an optional input signal to the module where the system is able to disable the radio from the module. The module host controller acts on this signal to remove power from the baseband and RF chips on the module. In that state the module will not respond to any message on the I²C control bus. From the USB port the module will report as being OFF.

This signal has an integrated pull-up resistor; if left unconnected the module stays ON by default.

7.3.5.4. PWR-EN

This Power Enable output signal is an indicator to the system host that the module is ON or OFF. When this pin is HIGH, the module is ON. When this pin is LOW, the module is OFF.

Before the system host can engage in I²C communication, it should verify that the module is ON by checking the PWR-EN pin.

7.3.5.5. BB_Status

The baseband status output signal is an indicator of the power-save mode of the baseband. When the module is in the full ON state, this pin is driven HIGH. When the module baseband is in Power-Save mode this signal is driven LOW.

7.3.6. Digital Video Output

7.3.6.1. HDMI Signals

This interface is a mapping of the module output to HDMI type of signals. This interface is 3.3 V and not level shifted (DCC_CLK and DDC_DAT are 5 V tolerant) and not ESD protected; as such the system must provide the remaining implementation to bring it to an HDMI-compliant interface.

- HPD is a 5 V tolerant input with a 15 k Ω pull-down.
- DDC_CLK is a 5 V tolerant, open drain I/O with a 2 k Ω pull-up to HDMI_5V.
- DDC_DAT is a 5 V tolerant, open drain I/O with a 2 k Ω pull-up to HDMI_5V.

An application example of the system connection to a Type A HDMI receptacle:



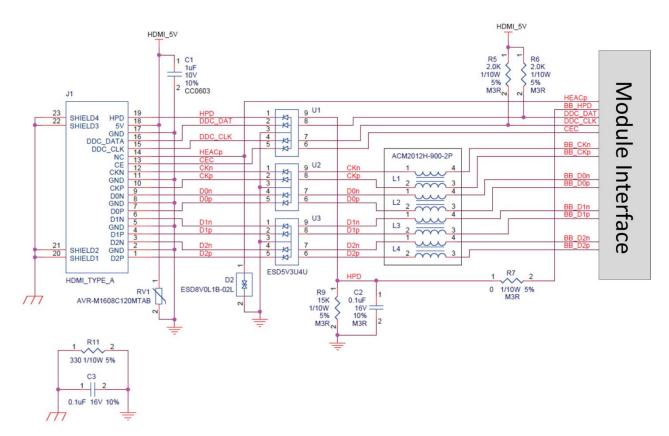


Figure 7.4. HDMI Signal Connection Example on System Board

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8. Power Management

The onboard microcontroller on the MOD6321-R receiver module handles most power management functions, relieving the system host controller of those tasks.

8.1. Power ON and Reset

The module is capable of operating standalone. The system just has to provide power to the module and the module initiates its power sequence automatically and internally. As such there is no power sequence required from the system standpoint.

The reset signal on the system interface RESETn is not required and should not be used by the system host for normal operation.

The module is equipped with an on board watchdog function, which monitors the baseband activity and can reset it if it stops responding. This function should not be implemented on the system host since it is already automatically done internally and could interfere with normal module operation.

In order to stop all WirelessHD activity on the module, the system can use the Module Disable signal (PWR_DISn). When the system host drives PWR_DISn LOW, the module immediately enters its OFF state. In this state, both the baseband and RF ICs are powered OFF.

When the system host releases PWR_DISn (driving it HIGH or floating it), the module automatically boots up and prepares for connection on its own.

8.2. Operational Details

There are three State Indicators from the module to the system which are used by the system host to exactly determine the state of the module at any point of time.

- PWR-EN signal indicates ON/OFF module status
- BB_STATUS signal indicates Power Save/ON module status
- LEDn signal indicates WirelessHD network status.

With these three signals the system host can follow the Table 8.1 below to determine the state of the module.

State	PWR-EN	BB_STATUS	LED <i>n</i>	I ² C	USB	Comments
No power	LOW	—	—	NO	NO	_
OFF	LOW	LOW	HIGH	NO	YES	Module controller is up but rest of the module is powered down.
Boot-up	HIGH	LOW	HIGH	NO	YES	Module controller is resetting and booting the baseband and RF chips.
Scanning	HIGH	HIGH	Slow blink	YES	YES	The first time the LED goes LOW after PWR-EN went HIGH, can be used to detect the I2C is ready for communication.
Associated	HIGH	HIGH	Fast blink	YES	YES	This is a WirelessHD state.
Power save	HIGH	LOW	HIGH	NO	YES	Network is maintained but no audio/video.
Connected	HIGH	HIGH	LOW	YES	YES	Video connection is ongoing.

Table 8.1. Module State Indicators

From Table 8.1 above, it can be concluded that BB_STATUS signal can be used to determine I²C control bus to the baseband is available for transactions from the system.

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8.3. Power State Description

The MOD6321-R receiver module has its own power management state machine. Here is a description of those states.

The actual protocol for those state transitions is described in the WirelessHD Specification and is not in the scope of this document. Refer to the WirelessHD Specification for further details.

When the module is:

OFF: It does not respond to any command, the SiI6321 and SiI6312 devices are powered OFF, and there is no radio transmission. Setting the PWR_DISn signal HIGH enables the module to leave this state.

ON: It can be in one of four states:

- **Module Reset:** This is a transitional state where the power controller asserts the baseband reset for a required duration, and if the module reset pin is disengaged, proceeds to the Module Scan state as soon as the radio is initialized.
- **Module Beacon:** The module radio has selected its channel and starts emitting beacons at 20ms intervals. The module stays in this state until a WirelessHD station finds this network coordinator and decides to join this network (association per WirelessHD specifications).
- Module Associated: This state is reached after the module has accepted a station; typically a transmitter in range, to join its network. This is accomplished when the station has sent an association request to the receiver and the coordinator has sent a positive association response back to the station.
 From this state, the module can take one of the two paths. The module can go to the Module Power Save state if no video connection has been established for a given period of time, or it can go to Module Connected state if a connection request has been requested and is accepted from both ends.
 The module also returns to this state after a video connection has been disconnected. The module indicates this state to the user or the system by a fast pulsing LEDn signal.
- Module Power Save: The Sil6321 and Sil6312 RF devices are saving power by shutting down some of their internal blocks. The module still emits beacons since it is the WirelessHD coordinator and updates the device list. This maintains the WVAN network integrity and allows AVC/CEC communication across the wireless link. This is particularly useful when a transmitter has no current active video connection but wants to remain available as possible audio/video source for the user. This allows the transmitter to be visible to the user in the TV input menu without consuming as much power as if it was actually transmitting video. The module indicates this state to the user or the system by setting the BB_STATUS signal LOW.
- **Module Connected:** In this state, the module is actively receiving audio and video across the WirelessHD high rate link from a transmitter. A connection request from either side has been granted by the coordinator and all protocol prerequisites for the setup of an audio video connection have been completed. The module indicates this state to the user of the system by setting the LEDn signal LOW.

8.4. Power Saving Mode

Power Saving mode is a specific state of the MOD6321-R receiver module where the Low Rate PHY (LRP) is active but the High Rate PHY (HRP) and Medium Rate PHY (MRP) data-paths are shut down. This allows the module to wake up at beacon time to transmit a beacon to the network devices, read Random Access Time Base (RATB) messages, and go back to sleep during the super-frame time. This mode of operation is defined in WirelessHD Specification. This effectively mode puts the Sil6321 network processor and Sil6312 transceiver in duty cycle at beacon time boundaries.

The benefit of this state is that even if the WirelessHD device has no active audio/video connection, it remains active in the network. The network coordinator can see it and initiate a connection. The WirelessHD device receives and forwards messages in the network.

In this state:

- I²C commands and messages are not available
- All Audio and Video path are shut down
- AVC/CEC commands are going through in both directions

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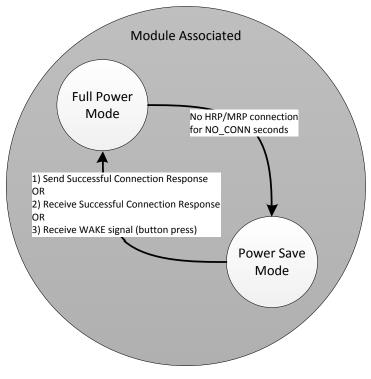


Figure 8.1. State Diagram for Power Save Mode

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SB-DS-02004-G



9. Software Interface

The MOD6321-R module does not require a dedicated host microcontroller on the system board for normal operation or factory testing. However, an I²C interface and set of commands are provided for high-level control of the module for optional use by the system manufacturer. This interface is not required for simple products such as a single port HDMI adapter, but it may be useful for more complex products which provide features such as graphic user interfaces or in-system firmware updates.

USB should not be used to control the module because this port is also dedicated for debugging, firmware programming, or factory testing functions. USB signals should be made accessible on the system side to allow those required functions.

9.1. I²C System Control

Application note for I²C is available upon request.

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10. Radio Considerations

This section describes the module radio frequency features.

10.1. Frequencies

The MOD6321-R receiver module operates in the unlicensed millimeter wave radio spectrum, also known as V-Band as defined by RSBG or EHF band defined by ITU.

WirelessHD defines four High Rate PHY (HRP) frequencies and the MOD6321-R receiver module is capable of operating in the two of them known as HR2 and HR3.

WirelessHD also defines subchannels known as Low Rate Phy (LRP). The MOD6321-R receiver module uses five of those in each HRP channels, which are called LR0, LR1, LR2, LR3, and LR4.

Each LR bidirectional channel is used for the establishment of one WirelessHD network. HR and MR channels are used for the high bandwidth audio/video connections. The isolation among LR, MR and HR is done by the time allocation such that they are not present at the same time within the same network.

This can be visually represented in Figure 10.1 below:

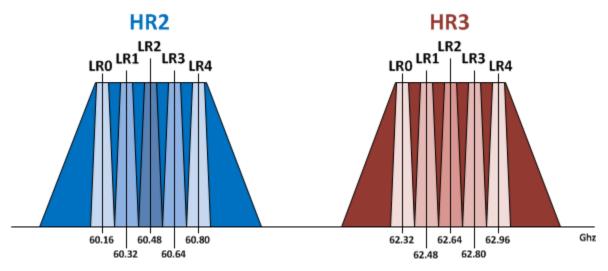


Figure 10.1. Frequency Spectrum of MOD6321-R Module

Here, it is simple to realize that there is no interference between any LR of the 10 LR channels and that HR Channels 2 and 3 are not interfering with each other. As such, it is possible to have up to ten different networks in a single room and two A/V links simultaneously without any collision. Because each HR link is high gain and very directional, it is in practice possible to install up to ten HR links (one per network) in the same room without any interference, due to spatial isolation, but it is safe to assume that one cannot guarantee there will not be interference in this extreme case.

10.2. Spectral Mask

The MOD6321-R receiver module spectral mask is tested and believed to be compliant with regulatory requirements in most geographic regions where the module can be legally purchased. Consult your Lattice Semiconductor sales representative or module manufacturer to ensure the intended sale geographic region for the end system is covered.

Use of the module frequency is tested and complies with the WirelessHD Specification as well as with other commercial specifications operating in the same band, such that interferences are not encountered.

The MOD6321-R receiver module has three PHYs available. LRP is the Low Rate PHY. MRP is the Medium Rate PHY. HRP is the High Rate PHY.

Figure 10.2, Figure 10.3, and Figure 10.4 on the next two pages are captures of the spectral mask for each of the three PHYs.

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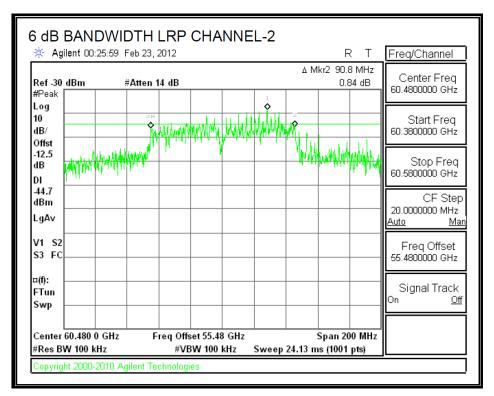
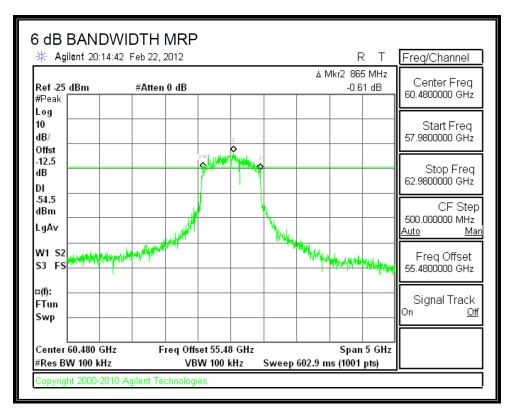


Figure 10.2. MOD6321-R Module LRP Spectral Mask Capture





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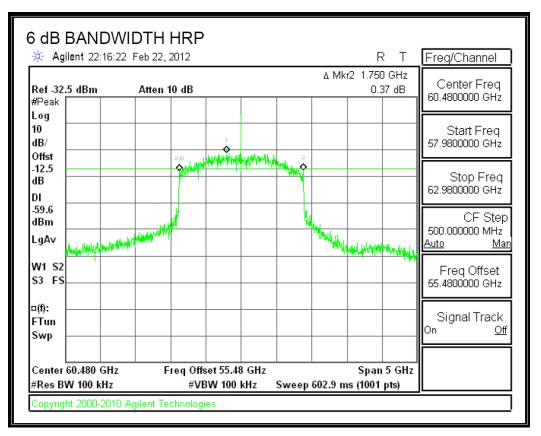


Figure 10.4. MOD6321-R Module HRP Spectral Mask Capture

10.3. WirelessHD Modes of Operation

The MOD6321-R receiver module operates within WirelessHD 1.1 specification using the PHY Modes described in Table 10.1. For compliance and test reason other PHY modes are actually supported by design but those are not used in the typical operation.

РНҮ	Rate	Modulation	Approximate Bitrate	Mode Index
HRP	Full	16-QAM	4 Gbps	2
HRP	Half	QPSK	2 Gbps	1
HRP	Quarter	QPSK	1 Gbps	0
MRP	Quarter	QPSK	0.5 Gbps	0
LRP	—	BPSK	5Mbps	2

Table 10.1. WirelessHD 1.1 PHY Modes in Typical Module Operation

The MOD6321-R receiver module supports use case number 1 as described in the WirelessHD 1.1 specification when connected to a WirelessHD transmitter module. This corresponds to one High Definition Audio/Video stream between a WirelessHD transmitter and a WirelessHD receiver.

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10.4. RF Chip Polarization Option

The WirelessHD receiver modules have two RF polarization options based on the RF chip mounted on the modules. When assembled with SiI6310 HRTR transceiver chip, the module operates in a single polarization mode. When assembled with SiI6312 HRRX receiver chip, the module operates in a dual polarization mode.

RF Chip	Polarization Mode	Polarization Loss at 0 degree	Polarization Loss at 90 degree	Module P/N
Sil6310	Single	0dB	12dB	MOD6321-R
Sil6312	Dual	3dB	4.5dB	MOD6321-R-12

Table 10.2. Effect of Polarization between Transmitter and Receiver Modules

To increase versatility of the receiver, the SiI6312 device gives more balanced results in all polarization situations. This is especially true for connection with mobile WirelessHD devices where use cases involve portrait and landscape screen views, implying both polarization should be expected.

10.5. Emissions

The MOD6321-R receiver module radiated emission is tested and believed to be compliant with regulatory requirements in most geographic regions where the module can be legally purchased.

Some regions have lower emission level acceptance than others; therefore a specific module configuration may be required for systems shipping in some locations.

Consult your Lattice Semiconductor Sales representative or module manufacturer to ensure the intended sale geographic region for the end system is covered by the module design before engaging in that market.

10.6. Modular Regulatory Approval

The MOD6321-R receiver module is subject to modular approval in some regulatory regions for mobile application as defined by FCC; that is to say, the module antenna in its normal operating condition is placed 200 mm away from the user. If the end system in which the module is integrated satisfies this requirement, it is possible that the system integrator can use SiBEAM modular grant of the module without having to obtain a new grant at system level, as far as the 60 GHz radio is concerned.

This implies the SiBEAM grant number at module level can be used at system level to simplify and expedite some of the regulatory tests and may greatly accelerate system integration and time-to- market.

Each sovereign regulatory region has representation bodies like FCC, IC, and ETSI, which govern the rules for modular approval submission in their respective regions. The MOD6321-R receiver module has been either pretested or received a grant at the module level in those regions, so module makers and systems integrator should determine if they can benefit from this.

For portable applications as defined by FCC, where the module antenna in its normal operating condition is placed less than 200 mm away from the user, additional requirements may apply in most regions, and may require additional testing and filing with special radio settings.

In most instances, SiBEAM has strong experience dealing with worldwide 60 GHz RF regulations and can guide module makers and system integrators; nonetheless it is the responsibility of module makers and system integrators to ensure that use of the MOD6321-R receiver module complies with local applicable laws and user installation guides accompanying any existing grants. For the final product that integrates the MOD6321-R module, it is the responsibility of the system integrator to make sure that the user manual of the final product with all the regulatory related instructions are provided to the consumer.

In order to make good use of the existing grants, module makers and module integrators must follow all conditions and restrictions listed in the grants and the final OEM installation manual/user guides attached to the grants.

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10.7. Grants and Labeling

The following grants have been issued for the MOD6321-R and MOD6321-R-12 modules:

Model: SII-SK63101:

- FCC (USA): UK2-SII-SK63101
- IC (Canada): 6705A- SIISK63101
- ETSI (EU)
- TELEC (Japan): 007-AA0106
- SRRC (China): 2016DJ0745
- KCC (Korea): MSIP-CRI-Lsb-SK63101 (MOD6321-R only)

Label Color: White Text Color: Black Material: adhesive and Time Resistant (Polyester) 40 mm Lattice Semiconductor Corporation Model: SII-SK63101 FCC: UK2-SII-SK63101 IC: 6705A-SIISK63101 4 BAR CODE and MAC address Area CMIIT ID: 2016DJ0745 mm -CRI-Lsb-SK63101 M R 007-AA0106 ODM or OEM marks: for example Made in Country Lot number, Product ID **Receiver Side** LABEI

Figure 10.5. Regulatory Label Example



11. Factory Testing

The MOD6321-R receiver module has a predefined factory setup configuration. This uses the reference USB connection and SWAM3 software suite, which includes test automation function called WiHD Validation Component.

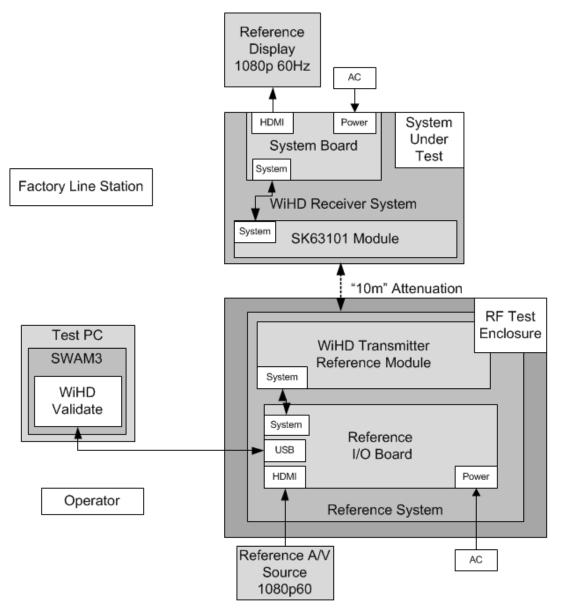


Figure 11.1. Defined Factory Setup for Adapters

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12. Performance, Certification, and Reliability Testing

The MOD6321-R receiver module has a predefined setup configuration in order to be tested for performance, WirelessHD Certification, and reliability tests. For those tests to be carried out, it is essential that the system provides access to the USB port of the module so corresponding software tools integrated in SWAM3 can be used. For this setup, the component WiHD configuration component is used.

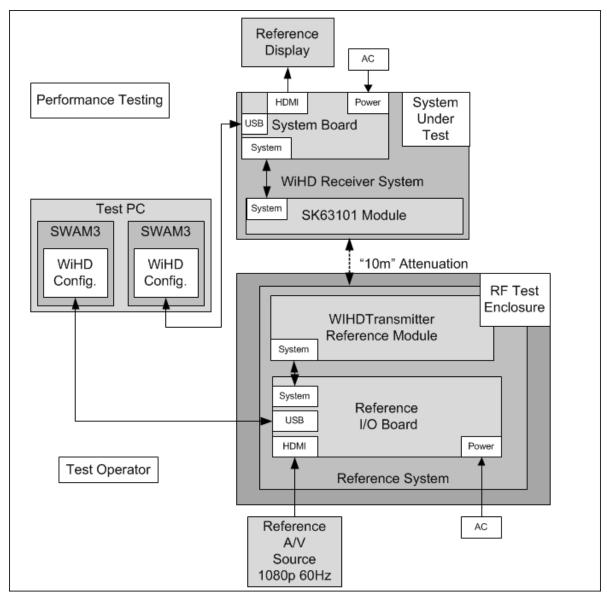


Figure 12.1. Defined Performance Test Setup

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13. Module Specifications

Table 13.1. Module Specifications

Parameter	Value
	Mechanical
Dimensions (in mm)	X = 17, Y= 74, Z = 5.4
Mounting Screw (x 2)	M2: ISO 7045:2011 M2x0.4 Pan head screws with type H or type Z
Mounting Post Diameter (in mm)	1
Max Module Case Temperature	90 °C at 60 °C ambient
System Connector	FPC, 0.4 mm pitch, 50 pin,
	Electrical
Power Input VIN	3.3 V DC, 1 A
Max Input Voltage Ripple	< 10% of VIN peak to peak
Control Serial Port	I ² C 400 kHz, 3.3 V
Max Power	3.3 W (max)
Connected Power	2.2 W (typical)*
Sleep/Wake Power	800 mW (max)
OFF, Disable Power	< 10 mA (max)
	Audio/Video
Maximum Pixel Clock	148.5 MHz
Maximum Video Format	1080p 60 Hz, 4:4:4 8 bits (MOD6321-R). 3840 x 2160 30 Hz, 4:4:4 8 bits (MOD6321-4K-R with Sil9396)
Minimum Video Format	480i 30 Hz, 4:2:2 8 bits
Audio formats (PCM)	Up to 8channel 96 kHz or 2chchannel 192 kHz
Audio formats (HBRA)	Up to 7.1 96 kHz or 2channel 192 kHz
	Radio Frequencies
HRP Channel Index Support	Channel 2, 3
Frequency Range HRP	59.40 GHz to 63.56 GHz
HRP Bandwidth	1.76 GHz per channel
LRP Channel Index Support	Channel 0, 1, 2, 3, 4 per HRP channel
LRP Bandwidth	92 MHz per channel
LRP Radiated Power EIRP	15.5 dBm
Receiver Sensitivity	–72 dBm (typical)
Noise Figure	10 dB (typical)
Line of Sight (LOS) Link Margin	10 dB at 10 m 1080p 60 Hz 4:4:4 with SPER < 0.01, with default setting
	Compliance and Regulation
WirelessHD1.1	Module tested with reference system (actual certification done at system level) Part 1, 2 and 3
HDMI	Module tested with reference system (actual certification done at system level) HDMI repeater category
CEC	Module tested with reference system (certification done at system level)
60 GHz Regulatory Modular Approval	CE, FCC, IC, KCC, SRRC, TELEC

*Note: With 1080p 60 Hz video, movie content @ 10 m.

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Table 13.2. Video Format Supported

Index	Resolution	Refresh Rate (Hz)	Color Depth (8, 10, 12 bits)	3D	CEA Code
1	640 x 480p (VGA)	60	RGB: 8-bit only	N	vesa
2	720 x 480p	60	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	N	2, 3
3	1280 x 720p	60	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	Ν	4
4	1920 x 1080i	60	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	N	5
5	720(1440) x 480i	60	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	Ν	6, 7
6	1920 x 1080p	60	RGB, Y444, Y422: 8-bit only	Ν	16
7	720 x 576p	50	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	Ν	17, 18
8	1280 x 720p	50	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	N	19
9	1920 x 1080i	50	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	N	20
10	720(1440) x 576i	50	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	N	21, 22
11	1920 x 1080p	50	RGB, Y444, Y422: 8-bit only	Ν	31
12	1920 x 1080p	24	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	Ν	32
13	1280 x 720p	120	RGB: 8/10/12, Y444: 8/10/12 Y422: 8-bit only	N	47
14	1920 x 1080p	48	RGB, Y444, Y422: 8-bit only	Ν	_
15	1920 x 1080p	24	RGB, Y444, Y422: 8-bit only	Frame Packing	_
16	1280 x 720p	60	RGB, Y444, Y422: 8-bit only	Frame Packing	_
17	1280 x 720p	50	RGB, Y444, Y422: 8-bit only	Frame Packing	_
18	1280 x 720p	60	RGB, Y444, Y422: 8-bit only	Top to Bottom	4
19	1920 x 1080i	60	RGB, Y444, Y422: 8-bit only	Side by Side	5
20	1280 x720p	50	RGB, Y444, Y422: 8-bit only	Top to Bottom	19
21	1920 x 1080i	50	RGB, Y444, Y422: 8-bit only	Side by Side	20
22	1920 x 1080p	24	RGB, Y444, Y422: 8-bit only	Top to Bottom	32
23	1280 x 720p	60	RGB, Y444, Y422: 8-bit only	Side by Side	4
24	1280 x 720p	50	RGB, Y444, Y422: 8-bit only	Side by Side	19
27	1024 x 768 (XGA)	60	RGB: 8-bit only	N	vesa



Table 13.2. Video Format Supported (Continued)

Index	Resolution	Refresh Rate (Hz)	Color Depth (8, 10, 12 bits)	3D	CEA Code
28	1280 x 1024 (SXGA)	60	RGB: 8-bit only	N	vesa
29	1680 x 1050 (WSXGA+)	60	RGB: 8-bit only	N	vesa
30	1920 x 1200 (WUXGA)	60	RGB: 8-bit only	N	vesa
31	800 x 600 (SVGA)	60	RGB: 8-bit only	N	vesa
32	848 x 480 (WVGA)	60	RGB: 8-bit only	N	vesa
	A	dditional Resolutions S	Supported By Sil9396 + MOD6321-4K-R		
33	3840 x 2160	24	RGB: 8-bit Y444: 8 bit Y422: 8 bit	N	
34	3840 x 2160	30	RGB: 8-bit Y444: 8 bit Y422: 8 bit	N	
35	2048 x 1536 (QXGA)	60	RGB: 8 bit	N	vesa
36	1920 x 1080p	120	RGB: 8-bit Y444: 8 bit Y422: 8 bit	N	63
37	1920 x 1080p	90	RGB: 8-bit Y444: 8 bit Y422: 8 bit	N	
38	2880 x 1440	60	RGB: 8 bit	N	
39	2048 x 1200	90	RGB: 8 bit	N	

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Table 13.3. Audio Format Supported

Format	<i>f</i> ₅ [kHz]	Ch	Speaker	HDMI <i>f</i> ₅ [kHz]	Comment
LPCM	32	2	_	_	_
LPCM	44.1	2	_	-	_
LPCM	48	2	_	-	_
LPCM	88.2	2	_	_	_
LPCM	96	2	_	-	_
LPCM	176.4	2	_	-	_
LPCM	192	2	_	-	_
LPCM	32	6	_	_	_
LPCM	44.1	6	_	-	_
LPCM	48	6	_	-	_
LPCM	88.2	6	_	-	_
LPCM	96	6	_	_	_
LPCM	32	8	_	_	_
LPCM	44.1	8	_	_	_
LPCM	48	8	_	-	_
LPCM	88.2	8	_	-	_
LPCM	96	8	_	_	_
SPDIF	32	6	_	_	_
SPDIF	44.1	6	_	_	_
SPDIF	48	6	_	-	_
SPDIF	88.2	6	_	-	_
SPDIF	96	6	_	-	_
SPDIF	32	8	_	-	_
SPDIF	44.1	8	_	_	_
SPDIF	48	8	_	-	_
SPDIF	88.2	8	_	-	_
SPDIF	96	8	_	_	_
Dolby Digital	48	2	LR	48	_
Dolby Digital	48	5.1	L R C LFE Ls Rs	48	_
Dolby Digital Plus	48	2	LR	192	-
Dolby Digital Plus	48	5.1	L R C LFE Ls Rs	192	_
Dolby Digital Plus	48	6.1	L R C LFE Ls Rs Cs	192	-
Dolby Digital Plus	48	7.1	L R C LFE Ls Rs Lsr Rsr	192	_
Dolby TrueHD	48	5.1	L R C LFE Ls Rs	768	HBR
Dolby TrueHD	48	6.1	L R C LFE Ls Rs Cs	768	HBR
Dolby TrueHD	48	7.1	L R C LFE Ls Rs Lsr Rsr	768	HBR
Dolby TrueHD	96	5.1	L R C LFE Ls Rs	768	HBR

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 Table 13.3. Audio Format Supported (Continued)

Format	<i>f</i> ₅ [kHz]	Ch	Speaker	HDMI <i>f</i> s [kHz]	Comment
Dolby TrueHD	96	6.1	L R C LFE Ls Rs Cs	768	HBR
Dolby TrueHD	96	7.1	L R C LFE Ls Rs Lsr Rsr	768	HBR
Dolby TrueHD	192	5.1	L R C LFE Ls Rs	768	HBR
Dolby MPEG2-AAC	48	2	LR	48	-
Dolby MPEG2-AAC	48	5.1	L R C LFE Ls Rs	48	-
DTS Digital Surround (Type1)	48	5.1	L R C LFE Ls Rs	48	_
DTS Digital Surround ES (Type1)	48	6.1	L R C LFE Ls Rs Cs	48	-
DTS Digital Surround 96/24 (Type1)	96	5.1	L R C LFE Ls Rs	48	-
DTS Digital Surround (Type4)	48	5.1	L R C LFE Ls Rs	192	_
DTS Digital Surround ES (Type4)	48	6.1	L R C LFE Ls Rs Cs	192	_
DTS Digital Surround 96/24 (Type4)	96	5.1	L R C LFE Ls Rs	192	-
DTS-HD High Resolution Audio	48	5.1	L R C LFE Ls Rs	192	-
DTS-HD High Resolution Audio	48	6.1	L R C LFE Ls Rs Cs	192	_
DTS-HD High Resolution Audio	48	7.1	L R C LFE Ls Rs Lsr Rsr	192	_
DTS-HD High Resolution Audio	48	7.1	L R C LFE Lss Rss Lsr Rsr	192	_
DTS-HD High Resolution Audio	96	5.1	L R C LFE Ls Rs	192	-
DTS-HD High Resolution Audio	96	6.1	L R C LFE Ls Rs Cs	192	_
DTS-HD High Resolution Audio	96	7.1	L R C LFE Ls Rs Lsr Rsr	192	-
DTS-HD High Resolution Audio	96	7.1	L R C LFE Lss Rss Lsr Rsr	192	-
DTS-HD Master Audio	48	5.1	L R C LFE Ls Rs	768	HBR
DTS-HD Master Audio	48	6.1	L R C LFE Ls Rs Cs	768	HBR
DTS-HD Master Audio	48	7.1	L R C LFE Ls Rs Lsr Rsr	768	HBR
DTS-HD Master Audio	48	7.1	L R C LFE Lss Rss Lsr Rsr	768	HBR
DTS-HD Master Audio	96	5.1	L R C LFE Ls Rs	768	HBR
DTS-HD Master Audio	96	6.1	L R C LFE Ls Rs Cs	768	HBR
DTS-HD Master Audio	96	7.1	L R C LFE Ls Rs Lsr Rsr	768	HBR
DTS-HD Master Audio	96	7.1	L R C LFE Lss Rss Lsr Rsr	768	HBR
DTS-HD Master Audio	192	2	LR	768	HBR
DTS-HD Master Audio	192	5.1	L R C LFE Ls Rs	768	HBR

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Ordering Information

Production Part Numbers:

Module	Part Number
MOD6321-R WirelessHD RX Module with Sil6310 RF Chip	MOD6321-R
MOD6321-R-12 WirelessHD RX Module with Sil6312 RF Chip	MOD6321-R-12
MOD6321-4K-R WirelessHD RX Module with Sil6310 RF Chip	MOD6321-4K-R
MOD6321-4K-R-12 WirelessHD RX Module with Sil6312 RF Chip	MOD6321-4K-R-12

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Appendix A. Network Operation, WirelessHD Video Area Network

The MOD6321-R module operates seamlessly in a context where multiple WirelessHD devices are in range. This mode of operation follows the WirelessHD specification and is often referred to as WVAN.

The WVAN rules are choices that SiBEAM has made for its module software implementation. The intention of those rules is to create a consistent user experience across different systems.

A.1. WVAN Rules

WirelessHD receivers always create competing WVAN networks. This is a design implementation choice, as it is conceivable that transmitters start their own networks or that receiver aggregate in one network, as supported by the WirelessHD specification. However, for practical purposes and simplification of user cases, receivers should set up their networks on adjacent channels. This implies that receivers are network coordinators and start beaconing as soon as they can.

WirelessHD transmitters always join an existing network and do not compete with receivers. This is an implementation choice, and implies that transmitters are WirelessHD stations, not coordinators. All WirelessHD devices have to support coordinator hand-over and all devices should be able to handle coordination of a network to comply with the WirelessHD specification.

The factory default for all devices is channels HR/MR = 2 and LR = 2 to ensure a consistent first-time experience for users, even in a network made of devices from different suppliers.

All devices are preloaded with a fixed radio channel priority order to maximize spreading of networks without interference.

Channel selection is automatic and sticky. This means that devices do not change channel setting unless they are forced to because another network occupies the channel. If a device is forced to change its channel setting, it gives up its current channel, moves to the next one in the channel order priority list, and does not attempt to go back to its original setting until all other channels are exhausted.

All receiver devices should be equipped with a user interface to reset to the factory default, switch transmitters, and monitor connection status. This implies that the minimum allowable user interface for a receiver consists of one button, using a long press for factory reset and a short press for switching video transmitters, and one LED to display status. The host system can make these functions, such as the DTV graphic user interface or menu, available when the module is integrated.

All transmitter devices should be equipped with a user interface to reset to the factory default, switch networks, and monitor device status. This implies that the minimum allowed user interface for a transmitter consists of one button, using a long press for factory reset and a short press for switching networks, and one LED to display status. When the module is integrated in a feature rich system, the system host may integrate those functions by other means: graphic display, remote control, and software user application; but in any case those functions have to be available to the user.



A.2. Transmitter State Flow Charts

Start State

When it starts, the transmitter has two possible behaviors:

1. If it has already been connected to a network, it tries to reassociate to this network or wait for this network to be re-established.

This has the following implication:

The transmitter will not join an existing network, if it is not the last one which it connected to. This avoids devices from roaming freely among networks.

The decision to go from one network to the other is user driven by triggering Next_WVAN command.

2. If it has never been connected to a network since last time it reset to default conditions, it associates to the strongest network available.

In this state, the decision flow is as follows.

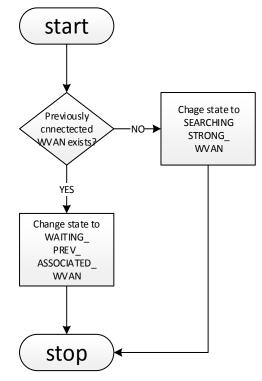


Figure A. 1. Transmitter Module Start State Flow

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Transmitter Searching the Strongest WVAN State

In this state, the transmitter has never been connected to a network since the last reset to "default", and the choice of network is based on proximity. The transmitter scans all channels and associate with the strongest (closest) receiver it finds.

In this state, the decision flow is as follows.

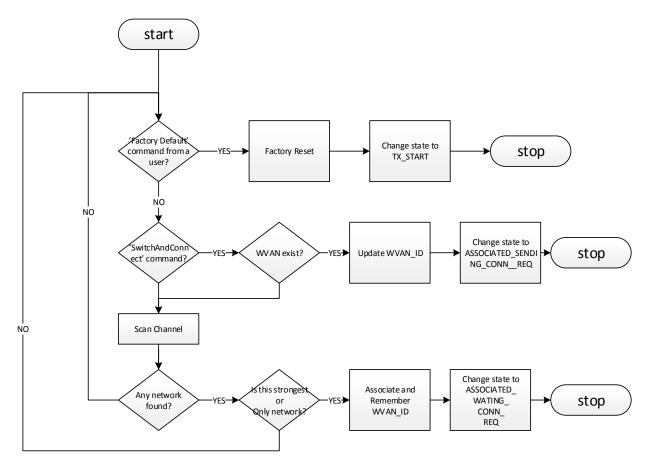


Figure A. 2. Transmitter Searching for the Strongest Network State Flow

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Transmitter Associated Waiting for Connect Request State

In this state, the transmitter is associated to a network. This means its device ID is registered to one coordinator. The transmitter is expected to be ready for a connect request coming from the application.

In this state, the decision flow is as follows.

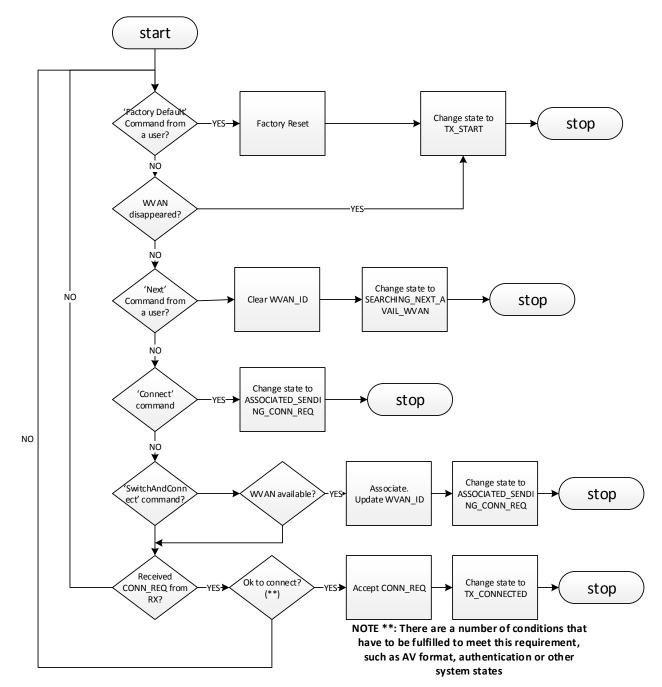


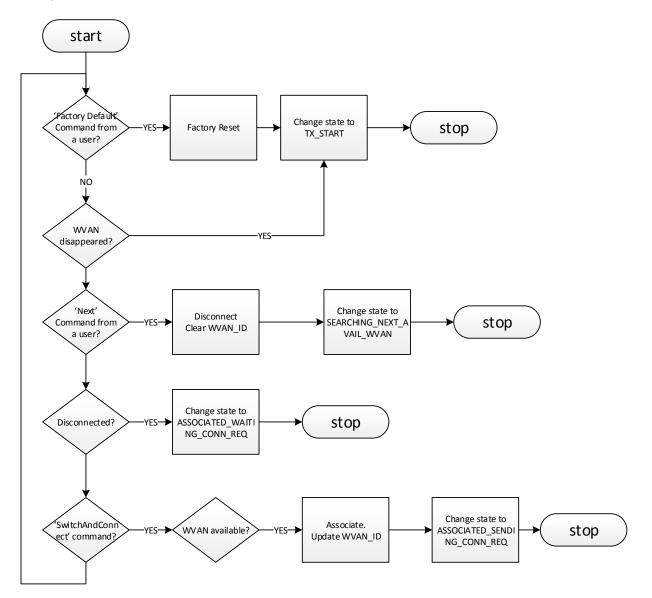
Figure A. 3. Transmitter Waiting for Connect State Flow

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Transmitter Connected State

In this state, the transmitter is connected to a receiver and an audio/video stream is being transmitted. In this state, the decision flow is as follows.





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Transmitter in Next_WVAN State

In this state, the user has triggered a Next_WVAN command, which forces the transmitter to abandon its current network and connection and search for next network available in the channel priority order predefined.

In this state, the decision flow is as follows.

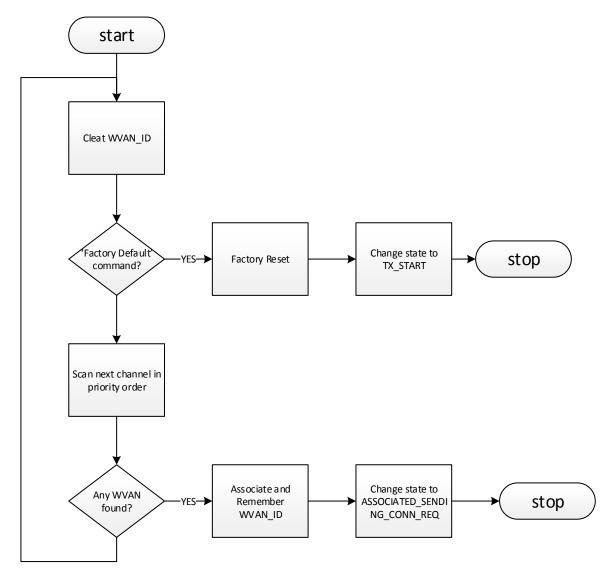


Figure A. 5. Transmitter Next_WVAN State Flow



Transmitter in Connect Request State

In this state, the transmitter is associated to a network and a connect request has been issued.

In this state, the decision flow is as follows.

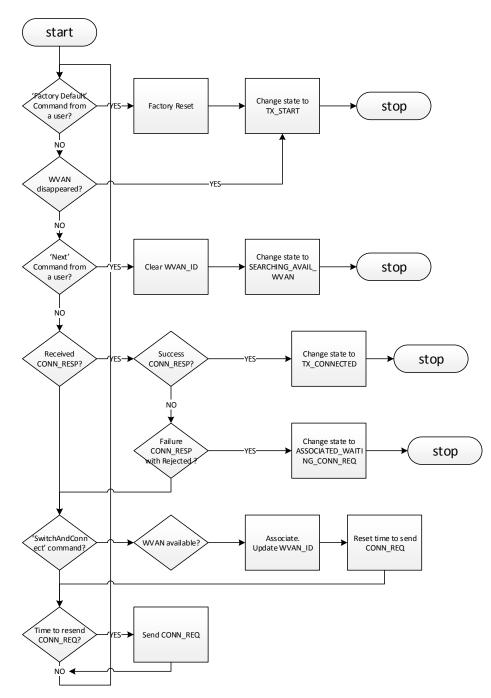


Figure A. 6. Transmitter Connect Request State Flow

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A.3. Transmitter States Diagram

The following diagram (Figure A. 7) illustrates the transition between the transmitters states described above.

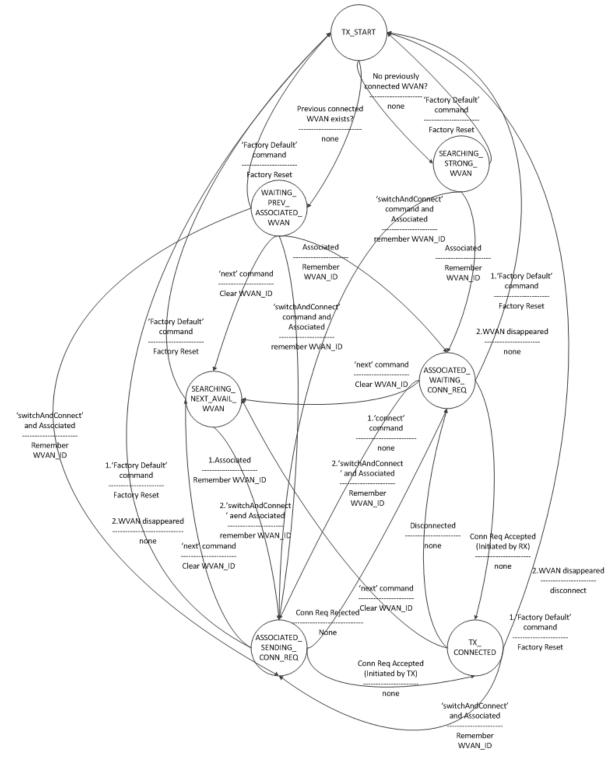


Figure A. 7. Transmitter State Diagram

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A.4. Receiver State Flow Charts

Receiver in Start State

When it starts, the receiver has two possible behaviors:

• If it has already been connected to a network, it tries to find its last transmitter and quickly move to a connection with it.

This has the following implication:

The receiver does not try to connect to any transmitter within range. It tries to reestablish the previous connection.

The decision to go from one transmitter to another is user-driven by triggering Next_Source command.

• If it has never been connected to a transmitter since the last reset to default, it associates to the first receiver found in range.

In this state, the decision flow is as follows.

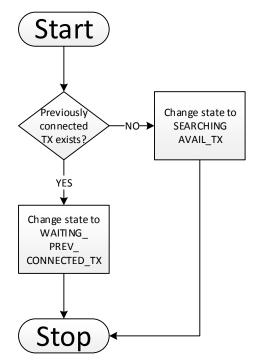


Figure A. 8. Receiver Module Start State Flow

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Receiver Searching for Transmitters State

In this state, the receiver is trying to find its target transmitter in the network such that it could reestablish an A/V connection.

In this state, the decision flow is as follows.

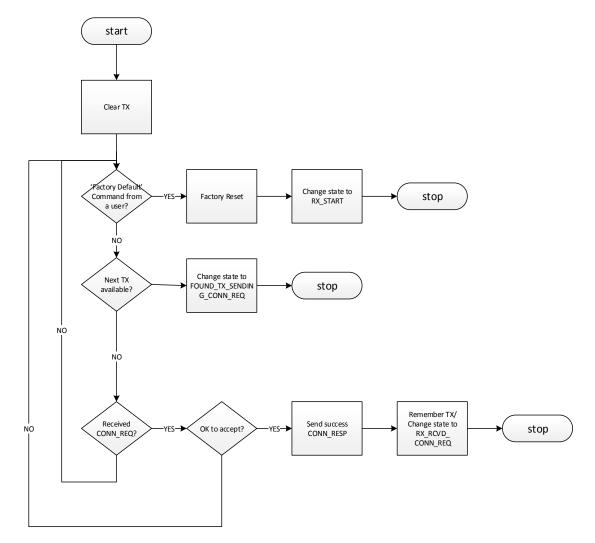


Figure A. 9. Receiver Module Searching for Transmitter State Flow



Receiver Waiting for Transmitter State

In this state, the receiver is waiting for its last connected transmitter to be available in the network. It does not try to connect to any transmitter in the network. It continues to wait for the reestablishment of the previous connection In this state, the decision flow is as follows.

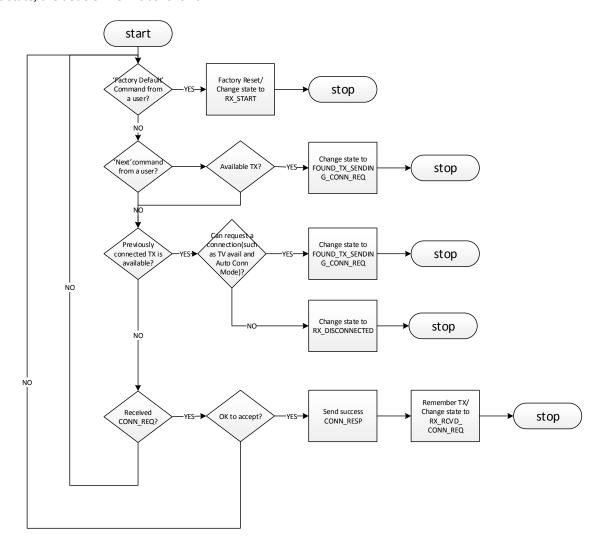


Figure A. 10. Receiver Module Waiting for Transmitter State Flow



Receiver Requests a Connection State

In this state, the receiver has selected a transmitter to which it should establish an A/V connection.

In this state, the decision flow is as follows.

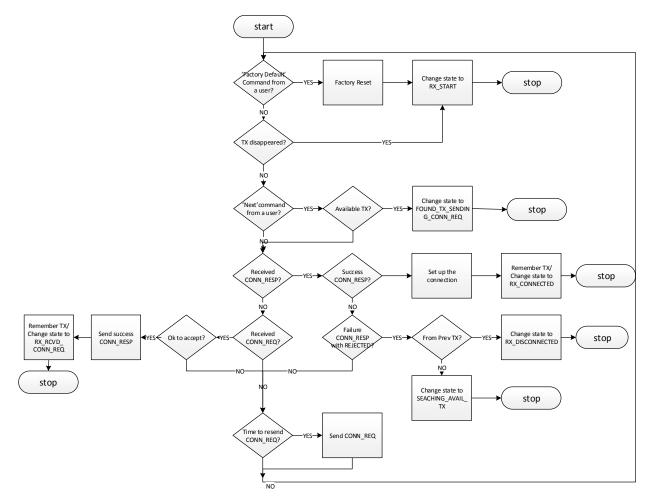


Figure A. 11. Receiver Requests a Connection State Flow

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Receiver Receives a Connection Request State

In this state, the receiver is receiving a connection request from a transmitter who wants to establish an A/V connection.

In this state, the decision flow is as follows.

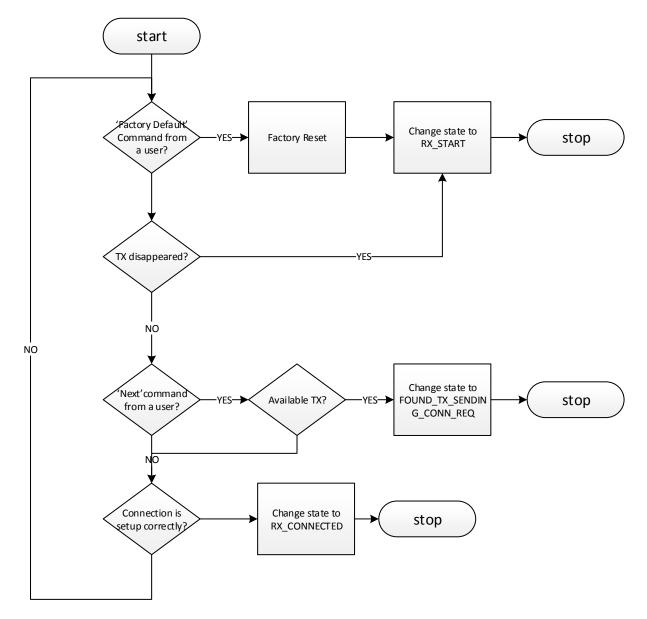


Figure A. 12. Receiver Receives Connection Request State Flow

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Receiver Connected State

In this state, the receiver is connected to a transmitter and streaming A/V. In this state, the decision flow is as follows.

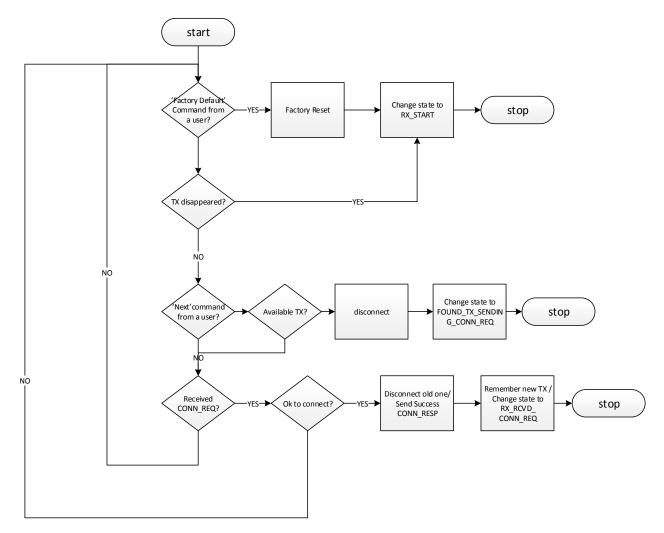


Figure A. 13. Receiver Module Connected State Flow

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Receiver Disconnected State

In this state, the receiver has the Audio Video link disconnected. This happens because an explicit disconnect request has been received or because the conditions for connection are not met. In this state, the decision flow is as follows.

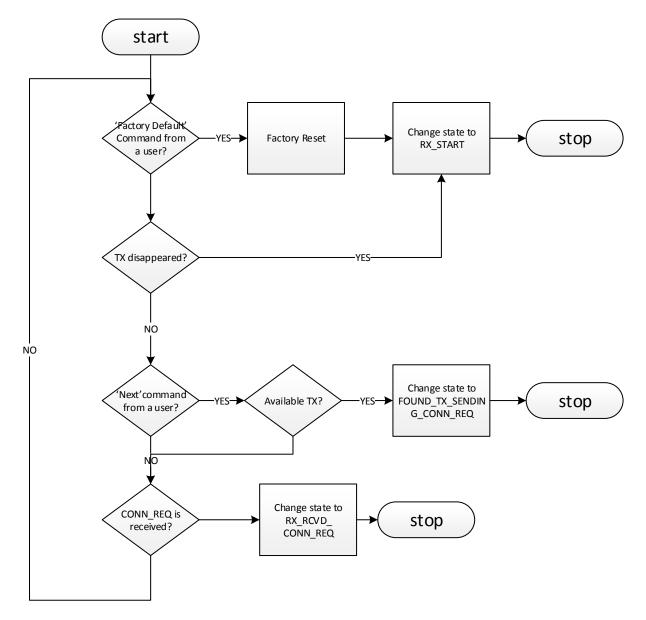


Figure A. 14. Receiver Module Disconnected State Flow

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A.5. Receiver States Diagram

The following diagram (Figure A. 15) illustrates the transition between the receivers states described above.

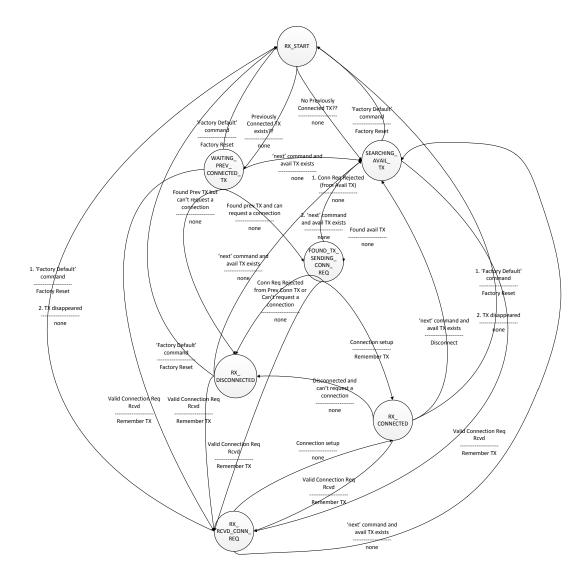


Figure A. 15. Receiver State Diagram

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A.6. Transmitter-receiver Sequences

The following diagrams (Figure A. 16, Figure A. 17, Figure A. 18, Figure A. 19, Figure A. 20, and Figure A. 21) illustrate the sequence of protocol exchange between transmitters and receivers in different common user cases.

Transmitter-receiver Basic Connection Flow

This describes the connection/disconnection flow sequence when one transmitter and one receiver are available, which is the most common case.

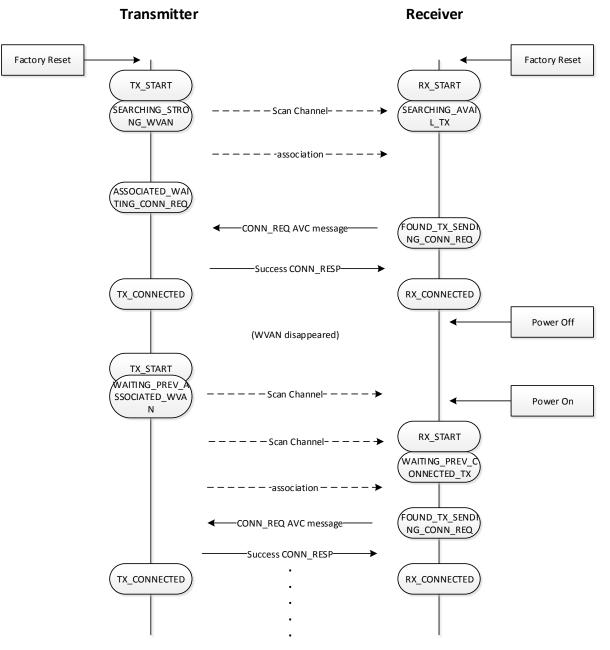


Figure A. 16. Basic Connection Sequence

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Transmitter Switches Network Flow

This section describes the case when a transmitter switches between one network and the other.

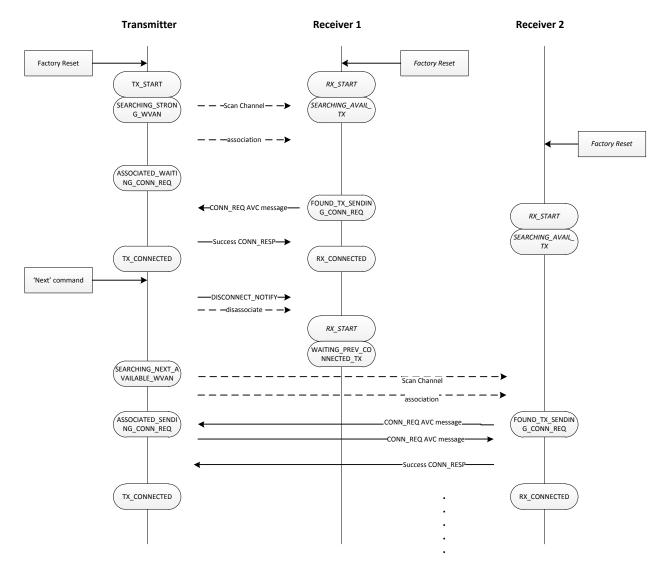


Figure A. 17. Transmitter Switches Network Sequence



Receiver Switches Transmitter

This section describes the case when a receiver switches its connection from one transmitter to another.

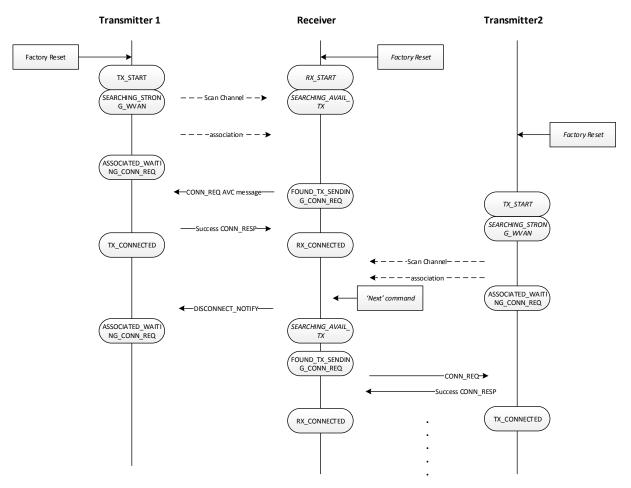


Figure A. 18. Receiver Switches Transmitter Sequence

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Transmitter Connection Preemption

This section describes the case when there is an established connection and the user establishes a new connection. This cancels the existing connection and creates a new one.

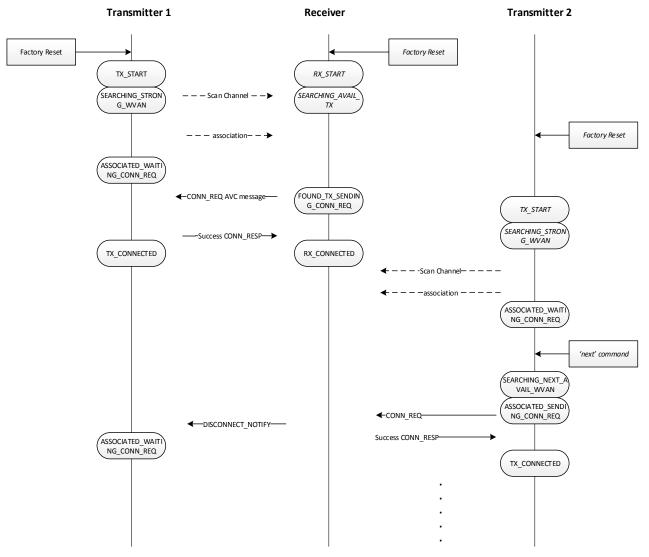


Figure A. 19. Transmitter Connection preemption

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Portable Transmitter Connection

Portable devices behave differently than stationary transmitters. A portable transmitter rejects automatic connection issued by receivers, but it can issue a connection request from the portable device manually.

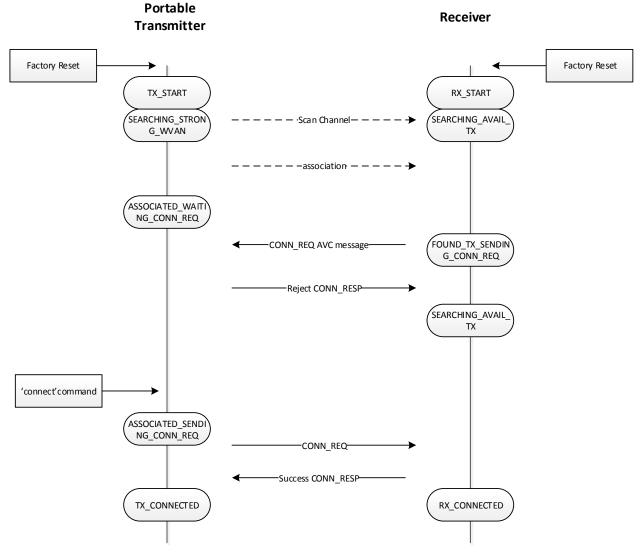


Figure A. 20. Portable Transmitter Connection



Portable Transmitter Network Switch

Portable devices behave differently than stationary transmitter. Portable transmitters can manually select the network which they are associated to the network.

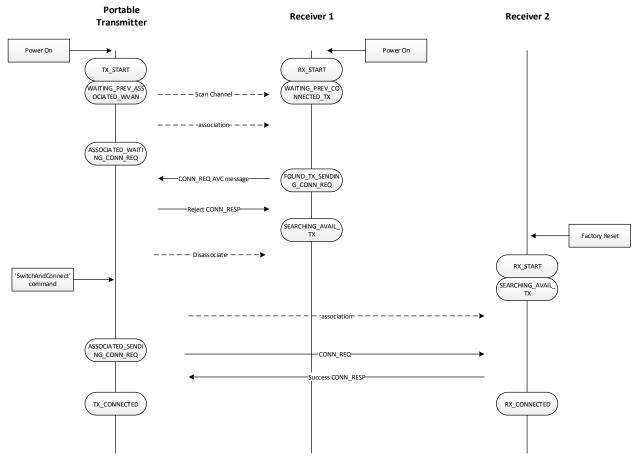


Figure A. 21. Portable Transmitter Network Switch

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Appendix B. OEM Installation for SII-SK63101/63102

B.1. Interference Statement

USA - Federal Communications Commission (FCC)

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy. If not installed and used in accordance with the instructions, it may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation.

- 1. This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.
- 2. This product does not contain any user serviceable components. Any unauthorized product changes or modifications will invalidate warranty and all applicable regulatory certifications and approvals.
- 3. Caution: Exposure to Radio Frequency Radiation: The installer of this radio equipment must place the module inside the enclosure of a stationary system intended to operate 20cm away from the end user body in typical operation. This implies that this module shall not be integrated in battery operated devices, handheld devices, wearable accessories for example.
- 4. The antenna(s) used for this transmitter must not be collocated or operating in conjunction with any other antenna or transmitter within a host device, except in accordance with FCC multi-transmitter product procedures. Other antennas shall be installed with a minimum 20 cm separation from the 60 GHz device antenna present in this module.
- 5. Caution: User is cautioned that changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.
- 6. FCC rule 15.255(a) prohibits the use of this device on aircrafts or satellites.
- 7. This module is intended for the OEM integrator.

Additional note to System Integrator/OEM:

The regulatory label on the final system must include the exact statement:

"Contains FCC ID: UK2-SII-SK63102" on the transmitter side enclosure

"Contains FCC ID: UK2-SII-SK63101" on receiver side enclosure.

Canada - Industry Canada (IC)

This device complies with RSS 210 of Industry Canada. Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of this device. "

L'utilisation de ce dispositif est autorisée seulement aux conditions suivantes : (1) il ne doit pas produire de brouillage et (2) l'utilisateur du dispositif doit étre prêt à accepter tout brouillage radio électrique reçu, même si ce brouillage est susceptible de compromettre le fonctionnement du dispositif.

The term "IC" before the equipment certification number only signifies that the Industry Canada technical specifications were met. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (EIRP) is not more than that required for successful communication. To prevent radio interference to the licensed service, this device is intended to be operated indoors and away from windows to provide maximum shielding. Equipment (or its transmit antenna) that is installed outdoors is subject to licensing.

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Pour empecher que cet appareil cause du brouillage au service faisant l'objet d'une licence, il doit etre utilize a l'interieur et devrait etre place loin des fenetres afin de fournir un ecran de blindage maximal. Si le materiel (ou son antenne d'emission) est installe a l'exterieur, il doit faire l'objet d'une licence.

Caution: Exposure to Radio Frequency Radiation.

- 1. The installer of this radio equipment must place the module inside the enclosure of a stationary system intended to operate 20cm away from the end user body in typical operation. This implies that this module shall not be integrated in battery operated devices, handheld devices, wearable accessories for example.
- The antenna(s) used for this transmitter must not be collocated or operating in conjunction with any other antenna or transmitter within a host device, except in accordance with FCC multi-transmitter product procedures. Other antennas shall be installed with a minimum 20 cm separation from the 60 GHz device antenna present in this module.

The regulatory label on the final system must include the exact statement:

"Contains IC: 6705A- SIISK63102" on the transmitter side enclosure

"Contains IC: 6705A- SIISK63101" on the receiver side enclosure

Europe - EU Declaration of Conformity and Restrictions

This equipment complies with the essential requirements and other relevant provisions of Directive 2014/53/EC.

This equipment is marked with the $\mathbf{\xi}$ symbol.

This indicates compliance with the Radio Equipment Directive 2014/53/EC and meets the relevant parts of following technical specifications:

Draft EN 301 567 v2.0.24: Broadband Radio Access Networks (BRAN); 60 GHz Multiple-Gigabit WAS/RLAN Systems; Harmonized EN covering the essential requirements of article 3.2 of the Radio Equipment Directive.

Final Draft EN 301 489-3 v2.1.1: Electromagnetic compatibility and Radio spectrum Matters (ERM); Electro Magnetic Compatibility (EMC) standard for radio equipment and services; Part 28: Specific conditions for wireless digital video links.

Draft EN 301 489-1 V2.2.0: ElectroMagnetic Compatibility (EMC) standard for radio equipment and services; Part 1: Common technical requirements; Harmonised Standard covering the essential requirements of article 3.1(b) of Directive 2014/53/EU and the essential requirements of article 6 of Directive 2014/30/EU

EN 60950: Safety of Information Technology Equipment.

EN 62311: 2008: Assessment of electronic and electrical equipment related to human exposure restrictions for electromagnetic fields (0 Hz - 300 GHz)

Marking by the symbol u on product packaging indicates that usage restrictions apply as indicated by Article 10(10) in the link below.

http://ec.europa.eu/info/law/better-regulation/initiatives/ares-2017-224238_en



The usage restriction in the EU applies to this device such that it is not permitted to be used for fixed outdoor deployments. The symbol shall be accompanied by names of countries affected as shown below.

		BE	BG	CZ	DK
		DE	EE	IE	EL
ES	FR	HR	IT	CY	LV
LT	LU	HU	MT	NL	AT
PL	РТ	RO	SI	SK	FI
SE	UK				

CE RF Radiation Exposure Statement:

- 1. Caution this equipment complies with European RF radiation exposure limits and is meant to be used as an Indoor device.
- 2. The installer of this radio equipment must place the module inside the enclosure of a stationary system intended to operate 20cm away from the end user body in typical operation. This implies that this module shall not be integrated in battery operated devices, handheld devices, wearable accessories for example.

Note for system integrators:

- The module is tested to comply with the requirement of the Radio Equipment Directive. System integrators are responsible for compliance of the final device with the Radio Equipment Directive.

- Packaging: CE Marking must also be on the outer packaging of the product. The outer packaging must also provide an indication as to where the device is intended to be used and/or conversely, where there may be restrictions for use.

B.2. Label and Location

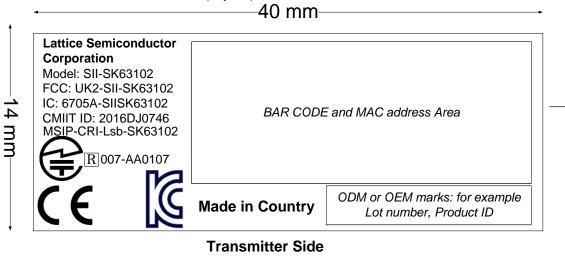
Label for Transmitter and Receiver Module

The module you receive from your vendor should have the following label attached to it:

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



Label Color: White Text Color: Black Material: adhesive and Time Resistant (Polyester)



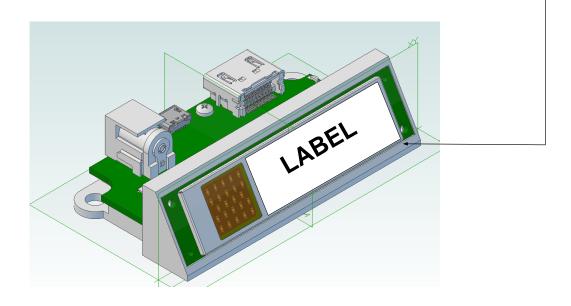


Figure B. 1. Label for SK63102 Transmitter Module



Label Color: White Text Color: Black Material: adhesive and Time Resistant (Polyester)

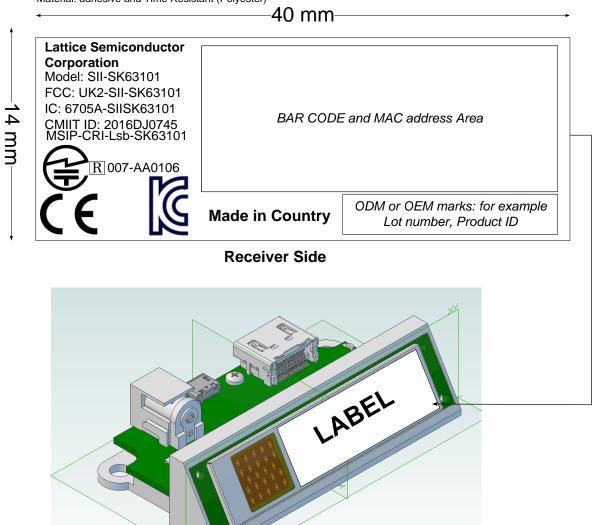


Figure B. 2. Label for SK63101 Receiver Module



B.3. Proposed Label and Location for Host Device

On Transmitter side:

This device contains:

Model: SII-SK63102 FCC ID: UK2-SII-SK63102 IC: 6705A-SIISK63102

(6

On Receiver side:

This device contains: Model: SII-SK63101 FCC ID: UK2-SII-SK63101 IC: 6705A-SIISK63101

Affix this label on the bottom of the host device enclosure, such that it is clearly readable by end user.



References

Standards Documents

This is a list of the abbreviations used in this document.

Abbreviation	Standards publication, organization, and date
WiHD	WirelessHD Specification, Version 1.1, WirelessHD, LLC, May 2010
HDMI	High Definition Multimedia Interface, Revision 1.4b, HDMI Consortium, March 2010
HCTS	HDMI Compliance Test Specification, Revision 1.4b, HDMI Consortium, March 2010
HDCP	High-bandwidth Digital Content Protection, Revision 1.4, Digital Content Protection, LLC; July 2009
HDCP-2.0	HDCP To WirelessHD Specification, Revision 2.0, Digital Content Protection, LLC; January 2010
DVI	Digital Visual Interface, Revision 1.0, Digital Display Working Group, April 1999
E-EDID	Enhanced Extended Display Identification Data Standard, Release A Revision 1, VESA; Feb. 2000
CEA-861-D	A DMONITOR Profile For Uncompressed High Speed Digital Interfaces, EIA/CEA, July 2006
EDDC	Enhanced Display Data Channel Standard, Version 1, VESA, September 1999

For information on the specifications that apply to this document, contact the responsible standards groups listed in on this list.

Standards Group	Web URL
WirelessHD	http://www.wirelessHD.org
ANSI/EIA/CEA	http://global.ihs.com
VESA	http://www.vesa.org
DVI	http://www.ddwg.org
HDCP	http://www.digital-cp.com
HDMI	http://www.hdmi.org

Lattice Semiconductor Documents

This is a list of the related documents that are available from your Lattice Semiconductor sales representative.

Document	Title
SB-DS-02003	MOD6320-T WirelessHD Transmitter Module Data Sheet

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



Revision History

Revision G, April 2018

Updated to latest template.

Revision F, January 2018

Added support of MOD6320-4K-T module to the General Description, Features, Module Specifications, and Ordering Information sections.

Revision E, August 2017

- Updated the Grants and Labeling section in response to 2014/53/EU.
- Updated the Europe EU Declaration of Conformity and Restrictions, Label and Location, and Proposed Label and Location for Host Device sections in response to 2014/53/EU.
- Updated Table 13.1. Module Specifications and Table 13.2. Video Format Supported.

Revision D, October 2016

Added one item to the Ordering Information section.

Revision C, March 2016

- Updated the Grants and Labeling section.
- Updated the Europe EU Declaration of Conformity and Restrictions, Label and Location, and Proposed Label and Location for Host Device sections.

Revision B, January 2016

Replaced SiI6310 with SiI6312 globally in this Data Sheet because of the product definition change.

Revision A, August 2015

First product release.

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